UTILITY PATENT APPLICATION TRANSMITTAL (Digitor new nonprovisional applications under 37 CFR 1.53(b))

250061US2S DIV Attorney Docket No.

First Inventor or Application Identifier Shinsuke SAKAMOTO

Title SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND STANDARD CELL

ð	See	APPLICATION ELEMENTS MPEP chapter 600 concerning utility patent application contents	Commissioner for Patents ADDRESS TO: Mail Stop Patent Application Alexandria, Virginia 22313			
1.		Fee Transmittal Form (e.g. PTO/SB/17) (Submit an original and a duplicate for fee processing)	ACCOMPANYING APPLICATION PARTS			
			7. Assignment was recorded at Reel 013433, Frame 0609			
2.		Specification Total Sheets 18	8. Application Data Sheet. See 37 CFR 1.76			
			9. (when there is an assignee) Power of Attorney			
3 .		Drawing(s) (35 U.S.C. 113) Total Sheets 4	10. English Translation Document (if applicable)			
,	_		11. ■ Information Disclosure Copies of IDS Statement (IDS)/PTO-1449			
4.		Oath or Declaration Total Pages 2	12. Preliminary Amendment			
	a.	☐ Newly executed (original or copy)	13. White Advance Serial No. Postcard			
	b.	Copy from a prior application (37 C.F.R. §1.63(d)) (for continuation/divisional with box 17 completed)	14. ☐ Certified Copy of Priority Document(s) (if foreign priority is claimed)			
		 i. DELETION OF INVENTOR(S) Signed statement attached deleting inventor(s) named in the prior application, see 37 C.F.R. §1.63(d)(2) and 1.33(b). 	15. Applicant claims small entity status. See 37 CFR 1.27			
5.		CD-ROM or CD-R in duplicate, large table or Computer Program (Appendix)	16. Other: Request for Priority			
6.	□ a.	Nucleotide and/or Amino Acid Sequence Submission (if applicable, all necessary) Computer Readable Form (CRF)				
	b.	Specification or Sequence Listing on :	•			
		i. CD-ROM or CD-R (2 copies); or				
		ii. Paper				
	C.	☐ Statements verifying identity of above copies				
17.	lf a	CONTINUING APPLICATION, check appropriate box, and supply	y the requisite information helew:			
•••	ŭ	Continuation Divisional Continuation-	•			
F		application information: Examiner: LEVIN, N.	Group Art Unit: 2825			
·						
For CONTINUATION OR DIVISIONAL APPS only: The entire disclosure of the prior application, from which an oath or declaration is supplied under Box 4b, is considered a part of the accompanying continuation or divisional application and is hereby incorporated by reference. The incorporation can only be relied upon when a portion has been inadvertently omitted from the submitted application parts.						
18. CORRESPONDENCE ADDRESS						
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Signature:		Date:	3/11/2004
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250061US2S DIV

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

NTOR(S) Shinsuke SAKAMOTO, et al.

SERIAL NO:

New Divisional Application

FILING DATE: Herewith

FOR:

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND STANDARD CELL PLACEMENT

DESIGN METHOD

FEE TRANSMITTAL

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

FOR	NUMBER FILED	NUMBER EXTRA	RATE	CALCULATIONS
TOTAL CLAIMS	8 - 20 =	0	x \$18 =	\$0.00
INDEPENDENT CLAIMS	2 - 3 =	0	x \$86 =	\$0.00
☐ MULTIPLE DEPENDENT CLAIMS (If applicable)			+ \$290 =	\$0.00
☐ LATE FILING OF DECLARATION			+ \$130 =	\$0.00
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A check in the amount of	to cover the filing fee is enclosed.	•	
Credit card payment form is attached to cover the filing fee in the amount of \$770.00			
The Director is hereby authorized to charge any additional fees which may be required for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to Depos Account No. 15-0030. A duplicate copy of this sheet is enclosed.			

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND, MAIER & NEUSTADT, P.C.

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